

**MOTOROLA****FACSIMILE****MOTOROLA LAW DEPARTMENT**DATE: January ^{12 kmv} 9, 2004TO: BOX ISSUE FEE (703) 305-8283
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*****Docket No: SC11697TP
Applicant: Rama Hedge et al.
Serial No.: 10/098,706
Art Unit: 2829
Filed: March 15, 2002
Class: 438-591000**ALL ITEMS MARKED WITH AN "X" ARE INCLUDED:**

1.	x	1 page Facsimile Cover Sheet
2.	x	1 page PTOL-85B Issue Fee Transmittal (in duplicate)
3.	X	1 page Letter to Examiner

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS
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UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S) Rama I. Hedge GROUP ART UNIT: 2829
APPLN. NO.: 10/098,706 EXAMINER: Asok K. Sarkar
FILED: March 15, 2002
TITLE: GATE DIELECTRIC AND METHOD THEREFOR

LETTER TO EXAMINER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The title of the above-referenced application has been amended in accordance with Examiner's Amendment (see page 2 of the Notice of Allowability). The correct title is "TRANSISTOR WITH LAYERED HIGH-K GATE DIELECTRIC AND METHOD THEREFOR."

Respectfully submitted,

SEND CORRESPONDENCE TO:

Motorola, Inc.
Law Department

Customer Number: 23125

By: 

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